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UNITED STATES PATENT DOCUMENTS

EXAM. INITIAL	DOCUMENT NUMBER	DATE	INVENTOR/ASSIGNEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
eh	5,189,685	02/23/1993	Jaffard et al.			
eh	5,970,110	10/19/1999	Li			
eh	6,157,694	12/05/2000	Larsson			
eh	6,281,759	08/28/2001	Coffey			

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FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO
eh	WO 01/45263 A1	06/21/2001	International			No

OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

eh	CRANINCKX J ET AL: "A 1.75-GHZ/3-V DUAL-MODULUS DIVIDE-BY-128/129 PRESCALER IN 0.7-MUM CMOS", IEEE JOURNAL OF SOLID-STATE CIRCUITS, IEEE INC. NEW YORK, US, VOL. 31, NO. 7, JULY 1, 1996, PAGES 890-897, XP000632373
Examiner (To be assigned)	Date: 4/5/05